

17224 U.S. PTO  
09/26/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Arun Ramakrishnan  
Title: Routing for Reducing Impedance Distortions  
Filing date: 2003.09.26  
Express mail number: EV 327 363 951 US  
Attorney docket: 03-1132  
Customer number: 24,319

UTILITY PATENT APPLICATION TRANSMITTAL

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

Sir:

Transmitted herewith for filing is the patent application described above. Enclosed are:

- ☒ Application cover sheet (1 sheet total).
- ☒ Specification (17 sheets total).
- ☒ Drawings (2 sheets total).
- ☒ Declaration and power of attorney (2 sheets total).
- ☒ Assignment with cover sheet (2 sheets total).
- ☐ Credit card charge authorization (1 sheet total).
- ☐ Information disclosure statement and PTO-1449 (2 sheets total).
- ☐ Copies of IDS citations (# documents total).
- ☐ Check in the amount of \$#.
- ☒ Return receipt postcard with all items specifically itemized.

	# Filed	# Extra	Small Entity Rate	Fee	Large Entity Rate	Fee
Basic Fee			@ 375	\$	@ 750	\$750
Total Claims	20 - 20	= 0	@ \$9	\$	@ \$18	\$
Independent Claims	3 - 3	= 0	@ \$42	\$	@ \$84	\$
Multiple Dependent Claims		= 0	+\$140	\$	+ \$280	\$
Total				\$		\$750

If a credit card authorization is not included herewith, or is defective, or if the credit card indicated in such authorization cannot be charged, the Commissioner is hereby authorized to charge payment of filing fees under 37 C.F.R. § 1.16 and processing fees under 37 C.F.R. § 1.17, and credit any overpayment to the LSI Logic Corporation deposit account number 12-2252.

2003.09.26

Date



Rick Barnes, 39,596

31108 U.S. PTO  
10/672538  
09/26/03